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MFORMATION DISCLOSURE STATEMENT BY APPLICANT

Substitute for form 1449B/PTO

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Sheet 2 of 2

Complete if Known				
Application Number	10/044,365			
Filing Date	January 11, 2002			
First Named Inventor	Wang, Xiaobao, et. al.			
Art Unit	2811			
Examiner Name	To Be Assigned			
Attorney Docket Number	015114-054910US			

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
OC	0002	Bendak, M. et al. (1996). "CMOS VLSI Implementation of Gigabyte/second computer network links," Dept. of Electrical and Computer Engineering, University of California at San Diego, La Jolla, CA 92093-0407, IEEE International Symposium on Circuits and Systems pp. 269-272.				
DC	- 0003	Boni, A. et al. (2001). "LVDS I/O Interface for Gb/s-per-Pin Operation in 0.35-um CMOS," IEEE Journal of Solid-State Circuits, 36(4):706-711.				
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PTO/SB/08A (10-01)

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Attorney Docket Number

U.S. PATENT DOCUMENTS							
		Document Number					
Examiner	Cite No.1	Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
90_	0001	US-6,147,520	11-14-2000	Kothandaraman et al.			
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	FOREIGN PATENT DOCUMENTS								
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	No.1	Country Code ³	Number ⁴	Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Applicant of Cited Document	Passages or Relevant Figures Appear	T ⁶	
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